

ECE 495 Spring 2020

Computer Engineering Design Laboratory

- Course Description:** This course emphasizes hardware design and debugging. Topics include combinational and sequential logic design using CAD tools, VHDL, and design based upon PLA/PLD.
- Instructor:** Dr. Edwin Hou
357 ECEC, (973) 596-3521
hou@njit.edu
- Lecture/Laboratory meet at:** M 10:00am–11:20am (ECEC 100) / Section 006, R 8:30am–12:50pm (FMH 204)
Section 004, R 1:00pm–5:20pm (FMH 204)
- Office hours:** W 10:00am-11:00am or by appointment
- Prerequisites:** ECE 353 and ECE 394
- Recommended Text:** J. Knoots, E. Hou, Laboratory and Supplementary Notes, ECE 495: Computer Engineering Design Laboratory, Ver. 3.0, ECE Dept. 2007.
This manual is for reference only. Refer to lecture notes for up to date information.
You will also need the Lab kit for ECE 394.
- Any VHDL book.
- Honor code:** The NJIT honor code will be upheld and that any violations will be brought to the immediate attention of the Dean of Students.

Course Learning Outcomes (CLO):

Student should be able to

1. design sequential circuits (using EEPROM) that meets a given design specification.
2. design hardware system that meets a given design specification and write VHDL program to implement them.
3. write lab reports documenting the results of the lab experiments.

Relevant ABET 1-7 Student Outcomes:

1. an ability to identify, formulate, and solve complex engineering problems by applying principles of engineering, science, and mathematics (CLO 1)
3. an ability to communicate effectively with a range of audiences (CLO 3)
6. an ability to develop and conduct appropriate experimentation, analyze and interpret data, and use engineering judgment to draw conclusions (CLOs 1, 2)

Grading Policy

Lab assessment:	5%
Quizzes & Final:	20%
Lab.:	60%
Demo:	40%
Report:	20%

Writing (10%)

Logic Diagram, Wiring Diagram, Software/Code comments,
Timing Diagram, etc. (10%)

Pre-Lab:	15%
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Logic Diagram/Tables, Wiring Diagram, Software/Code,
Timing Diagram, etc.

Out of the 75% for Pre-Lab and Lab.: Lab. 1, 3, 4, 5, 6 are 10% each; Lab. 2 is 5%. Lab. 7 is 20%.

Laboratory report is due one week after the lab is completed. A sample lab report and the grading rubric are available in moodle.

The quizzes will be conducted during the lecture.

Tentative Schedule

Meeting	Experiment
1	1
2	1
3	2
4	3 Quiz 1
5	3
6	4
7	4
8	5
9	5 Quiz 2
10	6
11	6
12	7
13	7
14	7
15	Final