

**Department of Electrical and Computer Engineering**

**New Jersey Institute of Technology**

**ECE 451 - Computer Systems Design (3 credits, 3 contact hours)**

**Instructor:** Bipin Rajendran; email: bipin@njit.edu; Tel.: 973-596-3516

**Text books** Computer Organization and Design, The Hardware/Software, 5th edition, Patterson and Hennessy, ISBN-13: 978-0124077263, ISBN-10: 0124077269

**Course Description:**

Focuses on advanced concepts in computer systems design, and the interaction between hardware and software components at various levels (i.e., hardware/software co-design). Introduces common performance measures used by hardware and software designers to facilitate comparative analysis. Main topics are: advanced pipelining, good instruction sets, CISC and RISC microprocessors, introduction to parallel computing, and a brief historical survey of computer designs.

**Prerequisite:** ECE students - ECE 353, instructor permission      **Corequisite:** none

**Specific course learning outcomes (CLO):** The student will be able to

1. Understand advanced concepts in computer systems design, and explain the interaction between hardware and software components at various levels
2. Understand and utilize the common performance measures used by hardware and software designers to facilitate comparative analysis;
3. Quantitatively evaluate and compare design of advanced pipelining, good instruction sets, CISC and RISC microprocessors, and parallel computing systems;
4. Understand the historical context of computer system designs and future trends.

**Relevant student outcomes (ABET criterion 3):**

- (a) an ability to apply knowledge of mathematics, science, and engineering (CLO 1, 2, 3)
- (b) an ability to design and conduct experiments, as well as to analyze and interpret data (CLO 2, 3)
- (c) an ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability (CLO 2, 3, 4)
- (g) an ability to communicate effectively (CLO 4)
- (h) the broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context (CLO 4)
- (i) a recognition of the need for, and an ability to engage in life-long learning (CLO 1, 2, 3, 4)
- (j) a knowledge of contemporary issues (CLO 3, 4)
- (k) an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice (CLO 3, 4).

**Computer assisted design and course specific software:**

SPIM, VHDL

This course outline serves to provide a big picture of the course. Instructional materials such as textbooks, individual topics, and grading policy are subject to revision and changes by individual instructors.

<b>Tentative Course Schedule</b>	<b>Weeks</b>
Introduction, Instruction Set Architecture	1
SPIM, SimpleScalar demo	2
Computer Arithmetic	3-4
Performance evaluation	5
CPU datapath and control	6-7
Pipelining	8-9
Memory Hierarchy	10-11
I/O Design	12-13
Parallel computing and Multiprocessors	14

**Grading policy:** Homework + Quizzes & Class Participation - 20%, Programming assignments/mini-projects - 25%, Midterm Exam - 25%, Final Exam - 30%

**Homeworks and projects**

SPIM and VHDL based simulations of computer architecture modules and concepts

**Updates and Assignments** to be distributed via Moodle

**Office hours, recitations and group studies:** By appointment

**Honor Code:** The NJIT Honor Code will be upheld; any violations will be brought to the immediate attention of the Dean of Students.

**Office:** ECE Bldg., Room 327

**Prepared by:** B. Rajendran

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